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ATTORNEY DOCKET NO. AUS920010995US1

In re Application of: Cook

Serial No. 10/047,801

Filed: January 15, 2002

For: Method, Apparatus, and Program
for a State Machine Framework

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Examiner: **Unknown**

Group Art Unit: **2151**

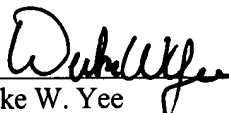
LETTER TO THE OFFICIAL DRAFTSMAN

Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

Submitted herewith are formal drawings (8 sheets) for the above-identified application.

Respectfully submitted,


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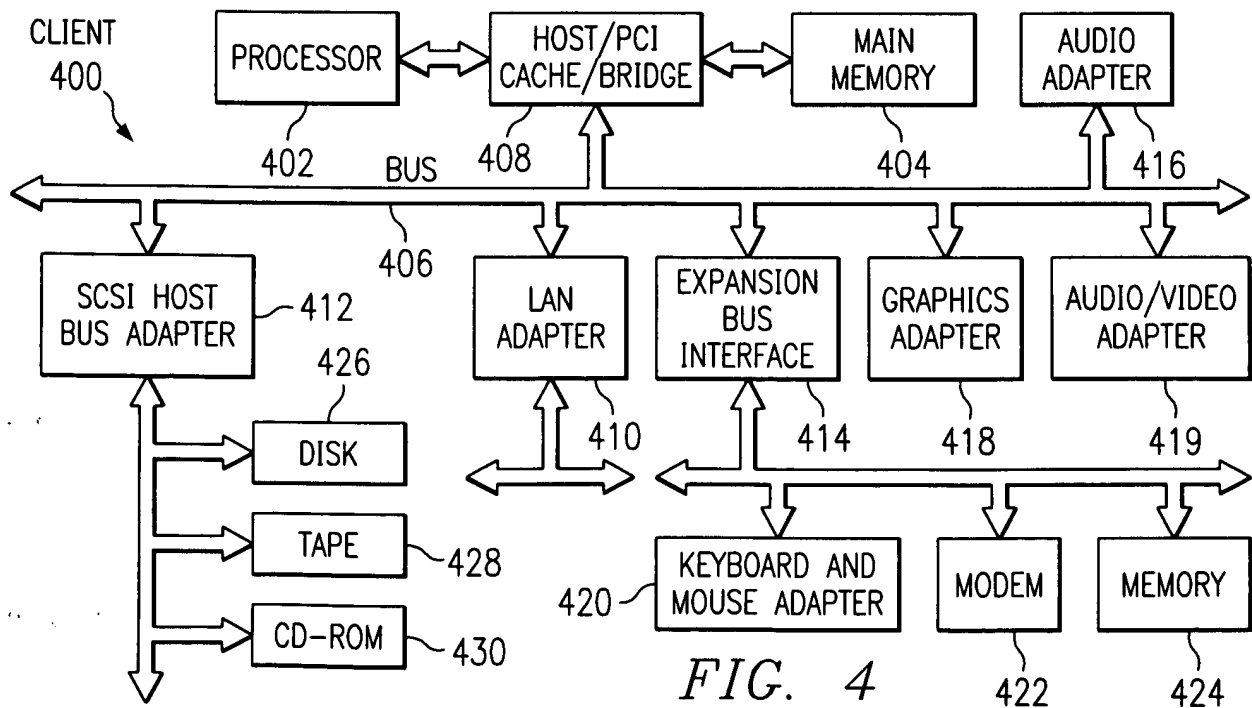
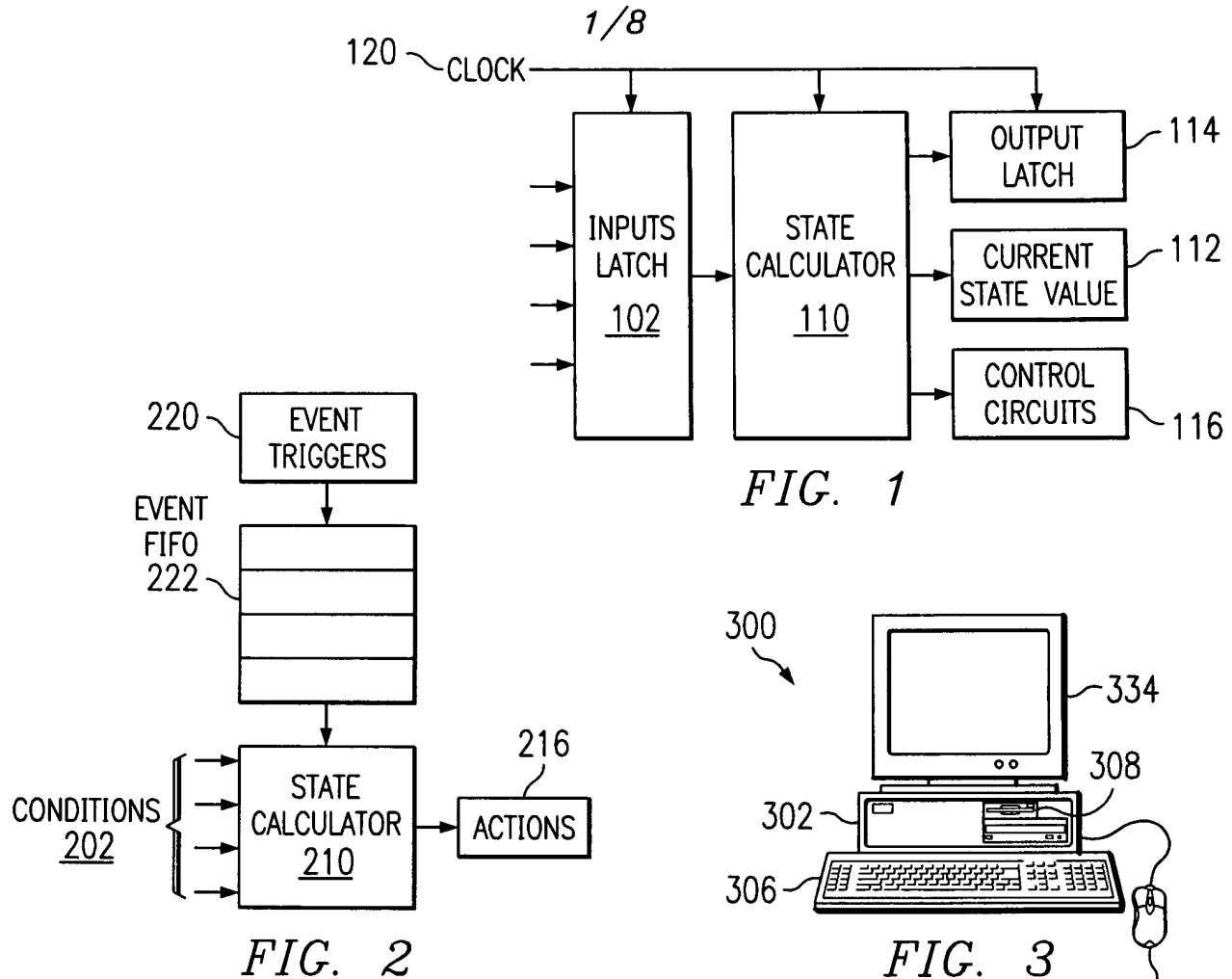
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on

03-11-02

by

Nell Whitten



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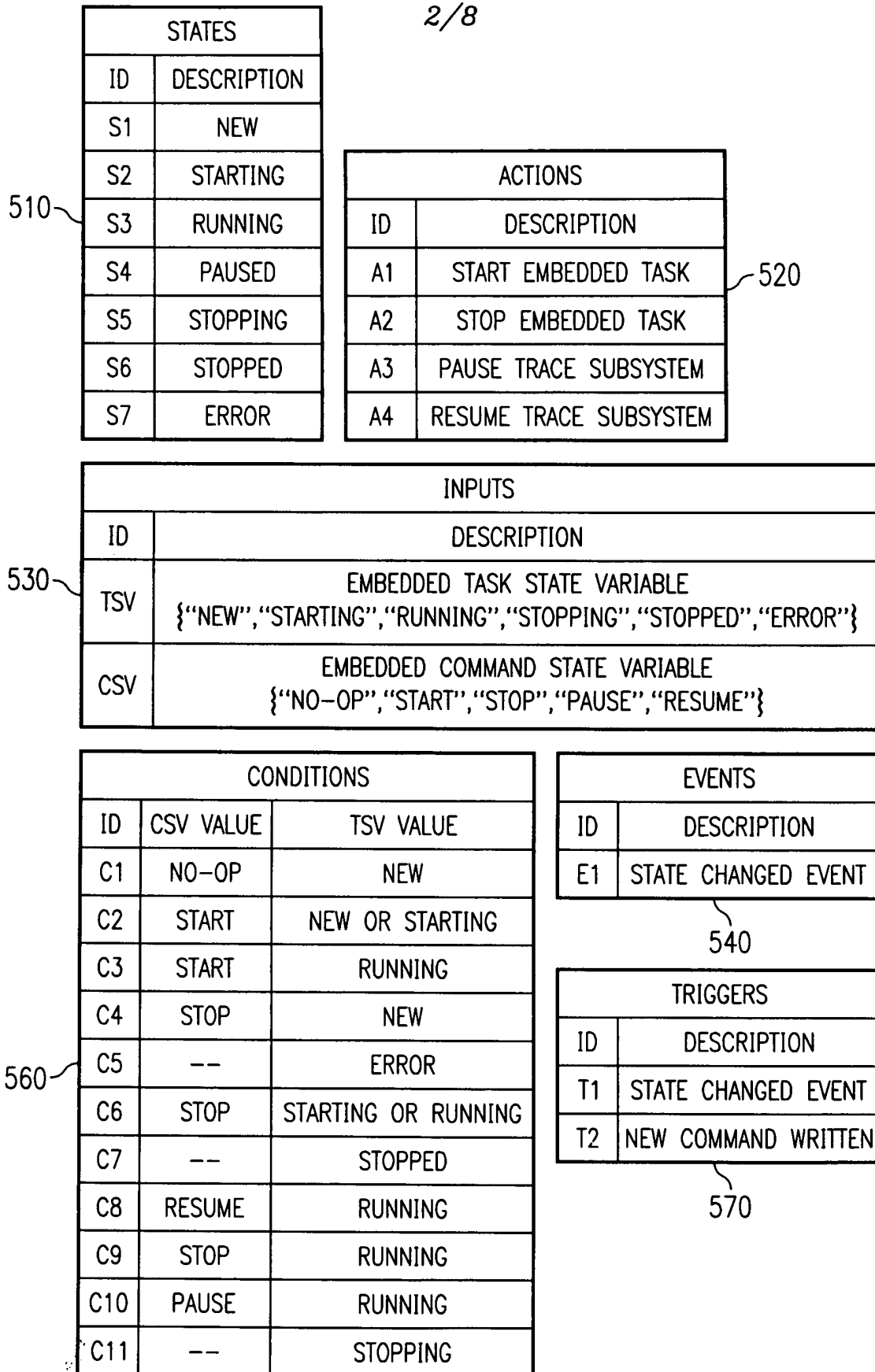


FIG. 5A

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FIG. 5B

STATE TABLE							
CURRENT STATE/NEXT STATE	S1	S2	S3	S4	S5	S6	S7
S1	C1 -- --	C2 A1 E1	C3 -- E1	X	X	C4 -- E1	C5 -- E1
S2	X	C2 -- --	C3 A4 E1	X	C6 A2, A3 E1	C7 -- E1	C5 -- E1
S3	X	X	C3 C8 -- -- -- --	C10 A3 E1	C9 A2, A3 E1	C7 A3 E1	C5 A3 E1
S4	X	X	C8 A4 E1	C10 -- --	C9 A2, A3 E1	C7 A3 E1	C5 A3 E1
S5	X	X	X	X	C11 -- --	C7 -- E1	C5 -- E1
S6	X	X	X	X	X	C7 -- --	X
S7	X	X	X	X	X	X	C5 -- --

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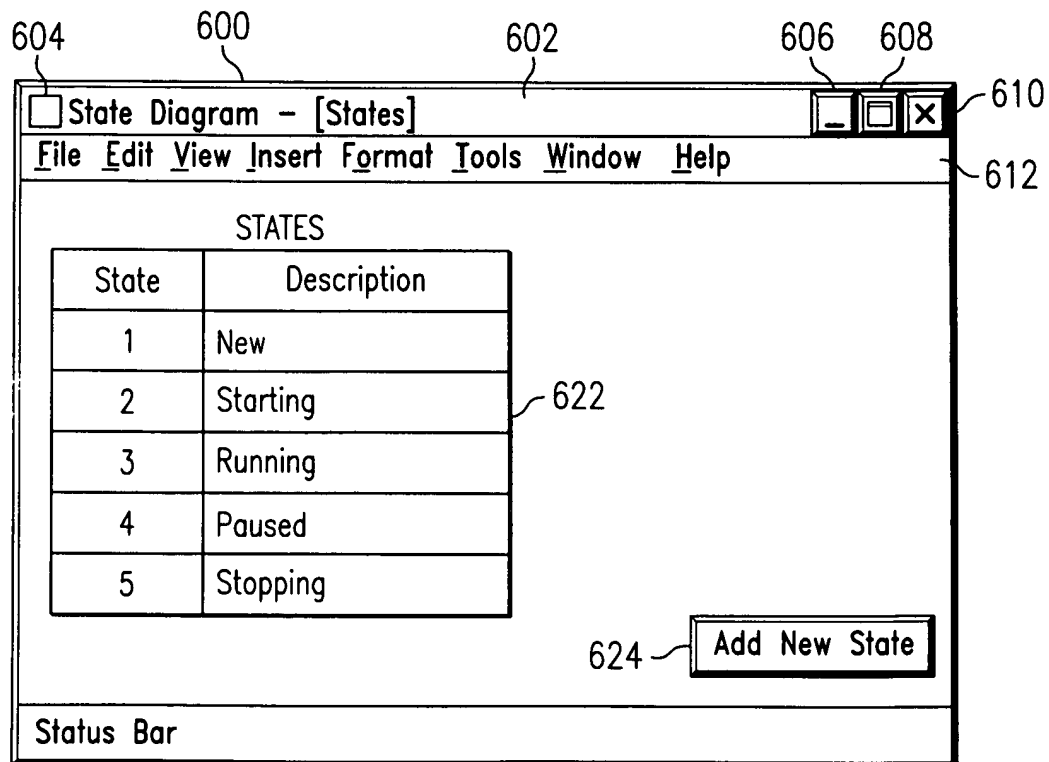
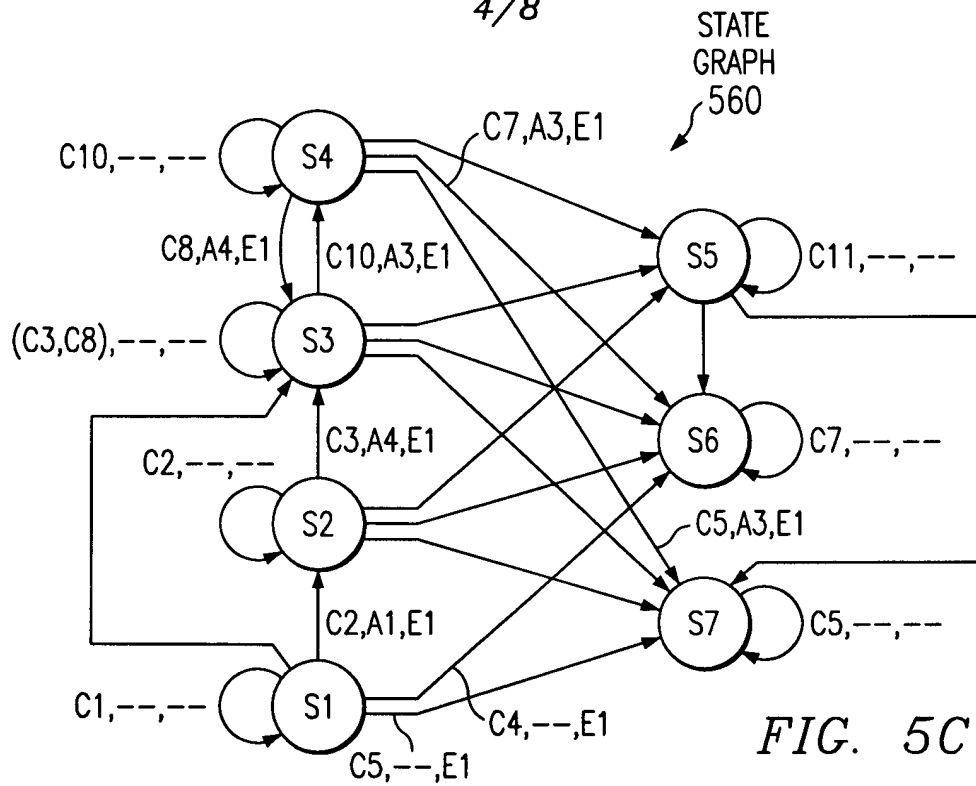


FIG. 6A

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FIG. 6B

630

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State Description

6 Error

Status Bar

FIG. 6C

640

642

644

STATE TRANSITIONS

Current State	Next State	T	C	Action
New	Starting	New	Start	Start Embedded Task

Add New State Transition

Status Bar

FIG. 6D

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NEW STATE TRANSITION

Current State	Next State	T	C	Action
New	Running	Running		

Start
Stop
Pause
Resume

Status Bar

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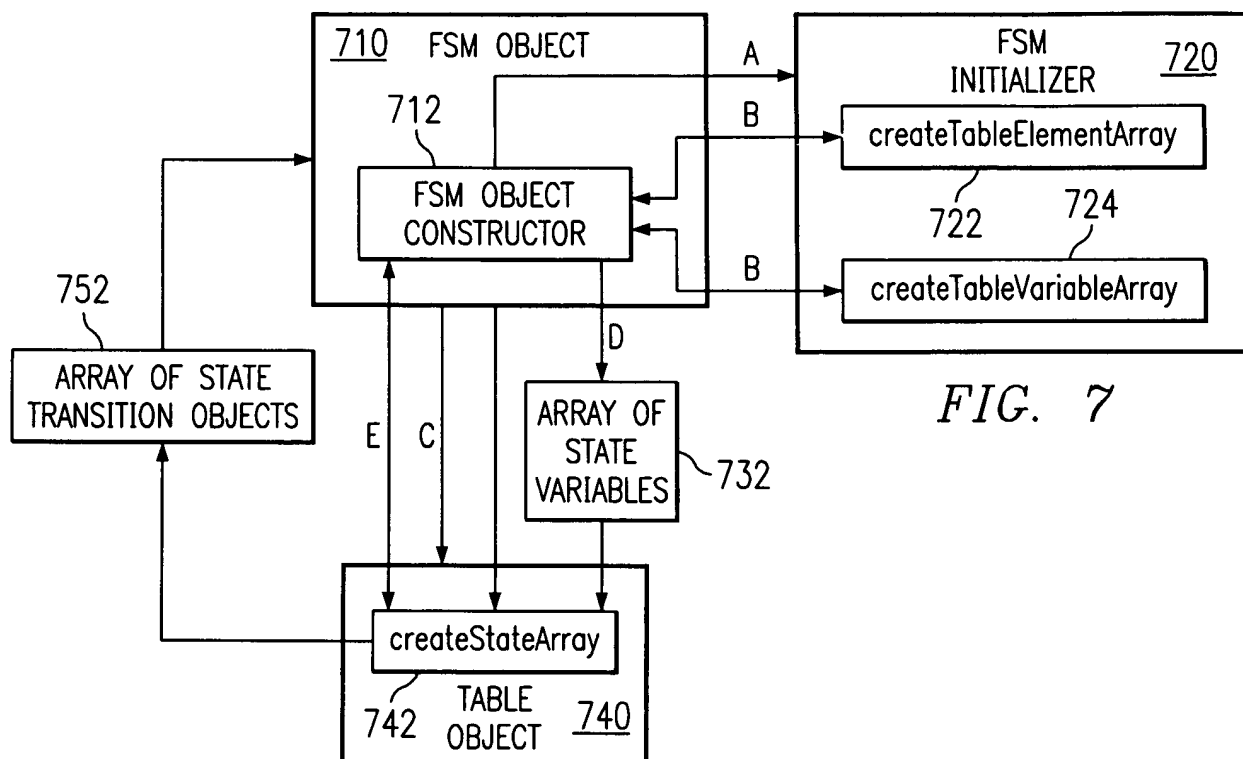


FIG. 7

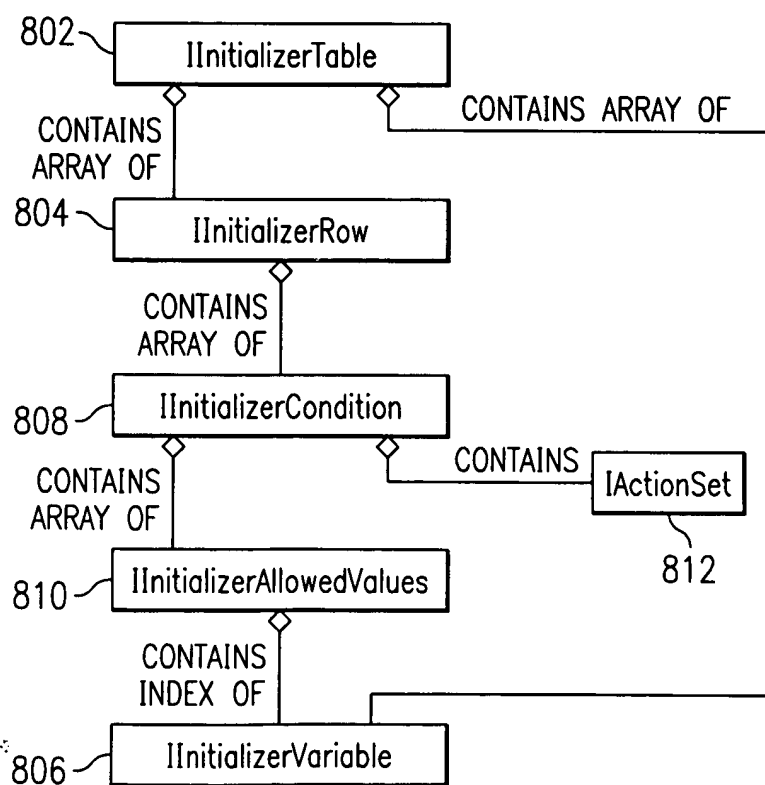


FIG. 8A

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FIG. 8B

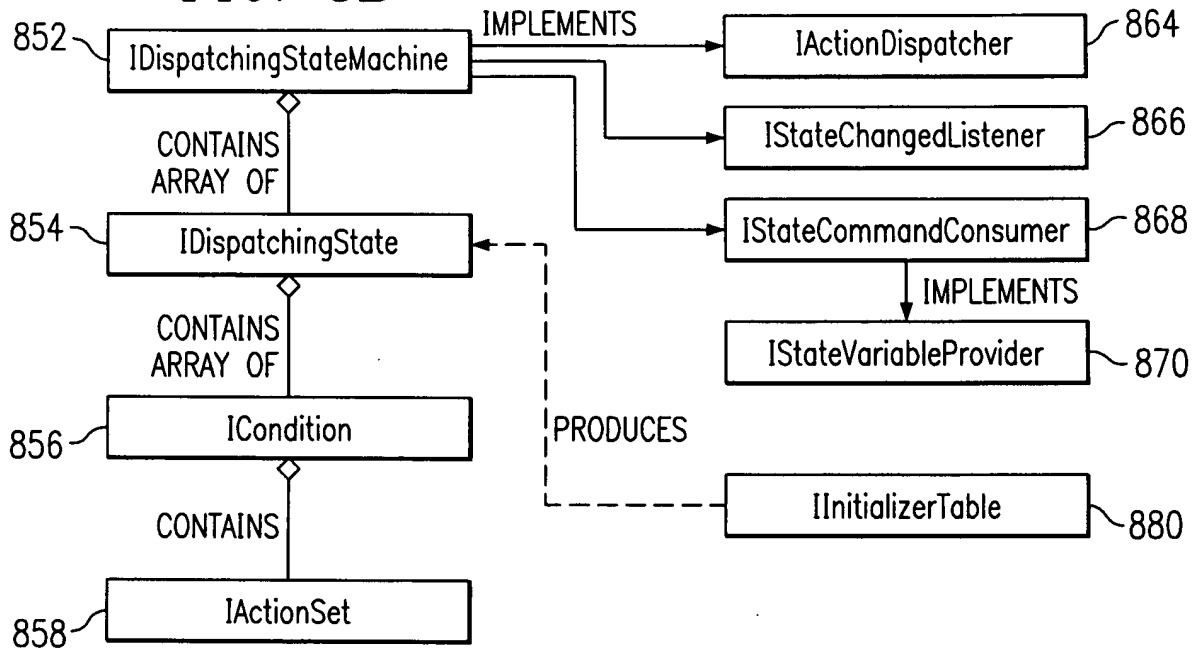
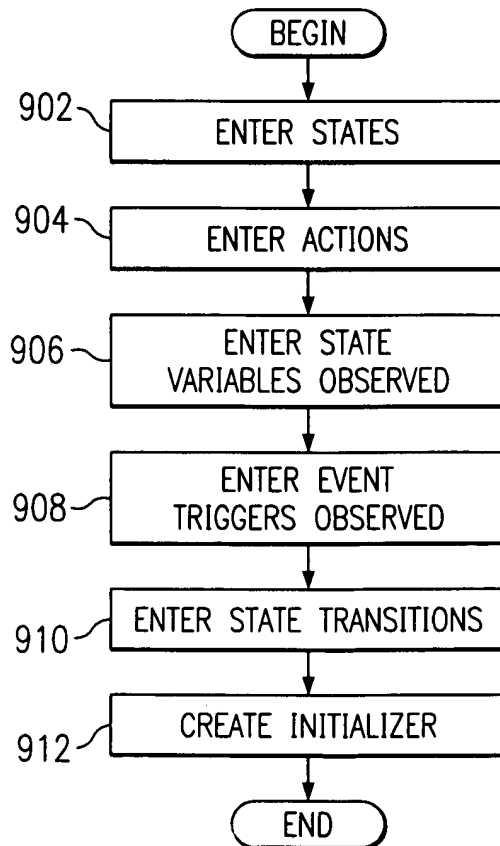


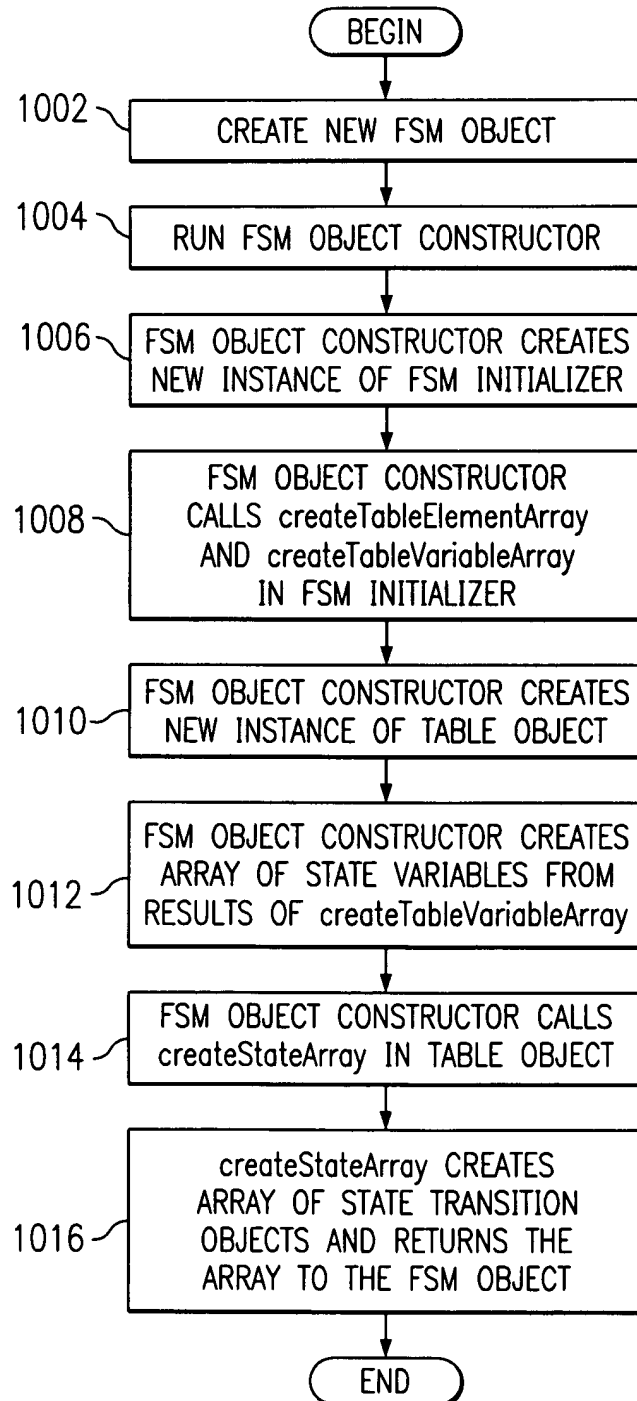
FIG. 9



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FIG. 10



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